

NOTICE OF APPEAL FROM THE PKIMARY EXAMINER TO THE BOARD OF PATENT APPEALS AND INTERFERENCES being denosited with the United States of the Correspondence is

APPLICATION

Inventor(S)

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Yan, Yifeng

Yen, Allen

Case:

addressed to: Commissioner of Patents and Trademarks Washington, D.C. 20231 on 3-6-16-11-25-02.

mailing paper or fee

being deposited with the United States Postal Service as first class mail in an envelope

Serial No.

09/653,295

Group Art Unit: 2811 Signature of person ma

Filing Date

August 31, 2000

Examiner:

Nguyen

Title

Stacked Structure For Parallel Capacitors And Method Of Fabrication

ASSISTANT COMMISSIONER FOR PATENTS **WASHINGTON, D. C. 20231**

SIR:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the decision dated August 26, 2002 of the Primary Examiner finally rejecting claims: 1-10.

The item(s) checked below are appropriate:

[]A Petition for Extension of Time for reply to the rejections is attached.

[X] Please charge the amount of \$320.00 covering the Appeal Fee (37 CFR 1.17(2)), to Agere Systems Deposit Account No. 501735. Duplicate copy of this letter is enclosed.

[]. Appeal Fee not required. (Fee was paid in prior Appeal.)

In the event of any non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 501735** as required to correct the error.

Please address all correspondence to Docket Administrator, Agere Systems Inc., 4 Connell Drive, Room 4U-533C, Berkelev Heights, NJ 07922-2747

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Date: November 25, 2002

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